

Fig. 1

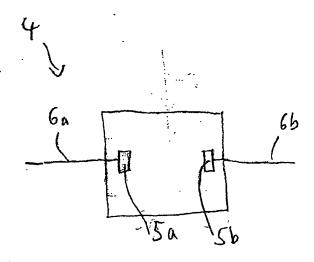


Fig. 2

| Select-<br>Signal | memory               |
|-------------------|----------------------|
| 01                | soft writing         |
| 10                | non-volatile writing |
| 11                | hard writing         |

Fig. 3

memory
mode

selection information bits to be stored in memory component
bits

TO 11

TO 11

To 100 10c

Tig. 4